

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE MATTER OF THE APPLICATION FOR PATENT

OF: Martin FISCHER

USPS EXPRESS MAIL

USSN: TO BE ASSIGNED -*NEW*

EV 338 198 822 US

FILED: February 4, 2004

FEBRUARY 04 2004

FOR: Circuit Arrangement for Signal
Detection Having Current Mirror
With Cascode Coupled Transistors

MS PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

February 4, 2004

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

- 1) Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 applicant encloses a Form PTO-1449 and copies of references AD to AI listed thereon.
- 2) In view of the waiver of the requirement (see 1273 OG 55, August 5, 2003), we are not enclosing a copy of the cited U. S. Patents (References AA to AC).
- 3) This statement is being filed simultaneously with the above identified new patent application.
- 4) References AA to AC, AF, *AH* & *AI* are in English and reference AD is accompanied by an English Abstract. References AC and AI have

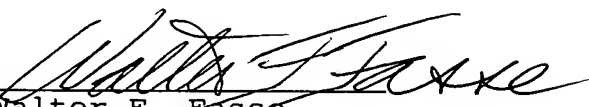
been discussed at pages 2 to 3 of the present specification. Reference AD corresponds to AA; AE corresponds to AB; and AG corresponds to AH from which the relevance can be determined in English. Therefore no further discussion of the references is required.

- 5) Applicant respectfully requests that the Examiner consider all references of record, return an initialled copy of the enclosed Form PTO-1449 and ensure that all references of record are printed on any patent issuing from this application.
- 6) Favorable consideration and allowance of claims 1 to 14 are respectfully requested.

Respectfully submitted,

Martin FISCHER
Applicant

WFF:ar/3495
Enclosure: postcard,
Form PTO-1449,
6 references,
1 English Abstract

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Sheet <u>1</u> of <u>1</u> LIST OF REFERENCES CITED BY APPLICANT (REVISED FORM PTO-1449) DATED: February 4, 2004										Atty. Docket No. 4360					Serial No.:				
										Applicant: Martin FISCHER									
										U.S. Filing Dat : February 4, 2004					Art Unit:				
U. S. PATENT DOCUMENTS																			
*EXAMINER INITIAL		DOCUMENT NO.							DATE	NAME	Cl.	Sub- Cl.	Fil. Date						
	AA	5	1	5	3	5	8	3	10/1992	Murdoch	-	-	-						
	AB	5	8	2	5	2	1	4	10/1998	Klosa	-	-	-						
	AC	5	8	8	9	4	8	9	03/1999	Friedman et al.	-	-	-						
FOREIGN PATENT DOCUMENTS																			
		DOCUMENT NO.							DATE	COUNTRY	Cl.	Sub- Cl.	Trans.						
	AD	3	8	5	4	1	2	9	12/1995	Fed. Rep. of Germany	-	-	=AA						
	AE	19	5	3	7	9	2	0	04/1997	Fed. Rep. of Germany	-	-	=AB						
	AF	1	0	2	6	8	3	2	08/2000	Europe	-	-	-						
	AG	10	0	1	2	6	3	7	09/2001	Fed. Rep. of Germany	-	-	=AH						
	AH	1	1	3	4	1	3	4	09/2001	Europe	-	-	x						
OTHER DOCUMENTS																			
	AI	D. A. Johns and K. Martin, "Analog Integrated Circuit Design", Chapter 6 "Advanced Current Mirrors and Opamps", section 6.1 "Advanced Current Mirrors", pages 256 to 259, ISBN:0-471-14448-7.																	
EXAMINER									DATE CONSIDERED										
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.																			

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